

[File] [Edit] [Tools] [Window] [Help]

(8) "4575744" | "4746966" | "4814503" | "5119168" | "5119169" | "5401989" |
 (0) 6326693.URPN
 (8) 6100573.URPN
 (0) 6544884.URPN
 (18) "4924285" | "4944836" | "5148674" | "5331204" | "5404046" | "5666007" |
 (13) 6100589.URPN
 (3) "5229642" | "5751065" | "5847466".PN.
 (18) 6100589.URPN, 6100573.URPN.
 (2) 6337517.pn.
 (1) 10/053810
 (525) "sakai yoshio".in.
 (6) "mimoto kenichiro".in.
 (0) "kenichiro mimoto".in.
 (370784) (multilevel\$4 multilayer\$4 (multi\$4 plural\$4) adj (layer\$4 level\$4))
 (4745522) wiring metal metalization metalization conduct\$3
 (38761) ((multilevel\$4 multilayer\$4 (multi\$4 plural\$4) adj (layer\$4 level\$4)) near2 (wi
 (111924) power adj2 (rail bus trace strip line)
 (215) ((multilevel\$4 multilayer\$4 (multi\$4 plural\$4) adj (layer\$4 level\$4)) near2 (wi
 (4259874) width thickness current
 (52) ((multilevel\$4 multilayer\$4 (multi\$4 plural\$4) adj (layer\$4 level\$4)) near2 (wi
 (2) 6383916.pn.

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UDC

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DBs: USPTO:US_POPUP:EPD:JPO:DERWENT:IBM:TDB

 Default operator: OR

 ((((multilevel\$4 multilayer\$4 (multi\$4 plural\$4) adj (layer\$4 level\$4)) near2 (wir
 Feb. 2004

#	Document ID	Issue Date	Title	Current D	Current XRef	Review	C	S	P	T	Image	Doc. D	P
1	<input type="checkbox"/> JP 200209424	2002039	Multilayer printed wiring board for direct flow brushle				<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 200209424	<input type="checkbox"/>
2	<input type="checkbox"/> JP 11298149 A	19991010	Multilayer wiring board for CSP, PGA, BGA packages,				<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 11298149	<input type="checkbox"/>
3	<input type="checkbox"/> US 4060693 A	19771117	AC superconducting transmission line - comprising high				<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4060693	<input type="checkbox"/>
4	<input type="checkbox"/> Aoki, Sachiko et US 5824570 A	19981029	Method for designing a semiconductor integrated circ	438/128	438/129		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5824570	<input type="checkbox"/>
5	<input type="checkbox"/> Armstrong, Don US 6692841 B	20040116	Thermal management for a vehicle mounted fuel cells	429/26	429/12		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6692841	<input type="checkbox"/>
6	<input type="checkbox"/> ASANO, TETSUJP 81013847 A	19880113	SEMICONDUCTOR INTEGRATED CIRCUIT		257/E21,575		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 81013847	<input type="checkbox"/>
7	<input type="checkbox"/> Beilstein, Jr, Ke US 5923181 A	19990716	Methods and apparatus for burn-in stressing and simul	324/758	324/760		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5923181	<input type="checkbox"/>
8	<input type="checkbox"/> Beilstein, Jr, Ke US 5688843 A	19971117	Methods and apparatus for burn-in stressing and simul	324/758	324/537		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5688843	<input type="checkbox"/>

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